

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kiichiro TANAKA Art Unit : Unknown
Serial No. : Not yet assigned Examiner : Unknown
Filed : April 27, 2001
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed with the application. Please apply any charges or credits to
Deposit Account No. 06-1050.

Respectfully submitted,

Date: April 27, 2001

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